

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	lock detecting circuit same first clock same second clock and clock near5 phase near10 (opposit\$4 or invert\$4) and (@ay<="2003"	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/29 07:38
L2	13	phase near5 lock same circuit same first clock same second clock and clock near5 phase near10 (opposit\$4 or invert\$4) and (@ay<="2003"	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/29 07:39
L3	171	phase near5 lock same circuit same clock and duty cycle same compar\$5 and (@ay<="2003"	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/29 08:10
L4	85	phase near5 lock same circuit same clock same (duration or time) near5 compar\$5 and (@ay<="2003"	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/29 08:37
S1	0	324/076.530.ccls.	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 15:01
S2	0	324/76.530.ccls.	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 15:01
S3	151	324/76.53.ccls.	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 15:02

S4	113	324/76.53.col.s. and @ay<="2004"	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 15:02
S5	2	JP-06112818-\$ did.	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 16:40
S6	2	JP-11289253-\$ did.	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 16:40
S7	2	JP-62043215-\$ did.	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 16:41
S8	208	lock detecting circuit	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 16:52
S9	190	lock detecting circuit and @ay<="2003"	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 16:52
S10	46	lock detecting circuit same phase near5 compar\$5 and @ay<="2003"	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 16:57
S11	20	lock detecting circuit same phase near5 compar\$5 and latch \$4 and @ay<="2003"	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 16:57
S12	12	lock detecting circuit same phase near5 compar\$5 and latch \$4 same output same lock\$4 and @ay<="2003"	US_PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 16:58

S13	5	lock detecting circuit same phase near5 compar\$5 and latch \$4 same output same lock\$4 and first clock and second clock and {@ay< = "2003"}	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 17:56
S14	1	lock detecting circuit and first clock and second clock same invert\$4 and {@ay< = "2003"}	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	OFF	2009/01/28 18:09

1/29/2009 9:11:39 AM

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